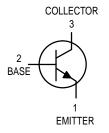
High Frequency Transistor NPN Silicon



MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	VCEO	12	Vdc
Collector-Base Voltage	VCBO	20	Vdc
Emitter-Base Voltage	VEBO	2.5	Vdc
Collector Current — Continuous	IC	50	mAdc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	PD	200 1.14	mW mW/°C
Total Device Dissipation @ T _C = 25°C Derate above 25°C	PD	300 1.71	mW mW/°C
Storage Temperature Range	T _{stg}	-55 to +150	°C

MPS5179

Motorola Preferred Device



$\textbf{ELECTRICAL CHARACTERISTICS} \ (T_{\mbox{\scriptsize A}} = 25^{\circ}\mbox{C unless otherwise noted})$

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector-Emitter Sustaining Voltage (I _C = 3.0 mAdc, I _B = 0)	VCEO(sus)	12	_	Vdc
Collector-Base Breakdown Voltage (IC = 0.001 mAdc, IE = 0)	V(BR)CBO	20	_	Vdc
Emitter-Base Breakdown Voltage (IE = 0.01 mAdc, IC = 0)	V(BR)EBO	2.5	_	Vdc
Collector Cutoff Current (V _{CB} = 15 Vdc, I _E = 0) (V _{CB} = 15 Vdc, I _E = 0, T _A = 150°C)	ІСВО	_	0.02 1.0	μAdc
ON CHARACTERISTICS				
DC Current Gain (I _C = 3.0 mAdc, V _{CE} = 1.0 Vdc)	hFE	25	250	_
Collector-Emitter Saturation Voltage (I _C = 10 mAdc, I _B = 1.0 mAdc)	VCE(sat)	_	0.4	Vdc
Base-Emitter Saturation Voltage (IC = 10 mAdc, IB = 1.0 mAdc)	VBE(sat)	_	1.0	Vdc

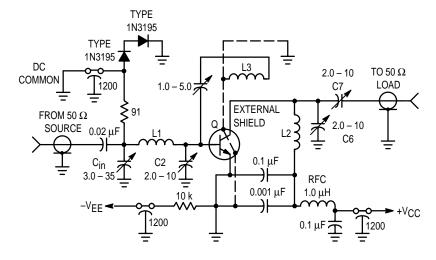
Preferred devices are Motorola recommended choices for future use and best overall value.



ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted) (Continued)

Characteristic	Symbol	Min	Max	Unit
SMALL-SIGNAL CHARACTERISTICS				
Current-Gain — Bandwidth Product ⁽¹⁾ (I _C = 5.0 mAdc, V _{CE} = 6.0 Vdc, f = 100 MHz)	fT	900	2000	MHz
Collector–Base Capacitance (V _{CB} = 10 Vdc, I _E = 0, f = 0.1 to 1.0 MHz)	C _{cb}	_	1.0	pF
Small Signal Current Gain ($I_C = 2.0 \text{ mAdc}$, $V_{CE} = 6.0 \text{ Vdc}$, $f = 1.0 \text{ kHz}$)	h _{fe}	25	300	_
Collector Base Time Constant (IE = 2.0 mAdc, V _{CB} = 6.0 Vdc, f = 31.9 MHz)	rb′C _C	3.0	14	ps
Noise Figure (See Figure 1) (I _C = 1.5 mAdc, V _{CE} = 6.0 Vdc, R _S = 50 ohms, f = 200 MHz)	NF	_	5.0	dB
Common–Emitter Amplifier Power Gain (See Figure 1) (V _{CE} = 6.0 Vdc, I _C = 5.0 mAdc, f = 200 MHz)	G _{pe}	15	_	dB

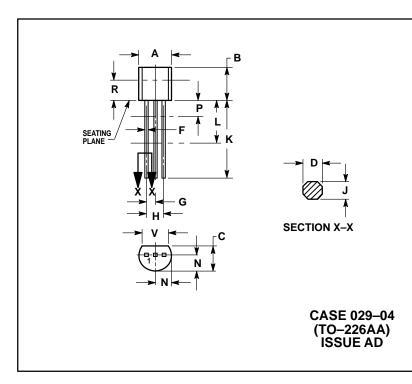
^{1.} f_T is defined as the frequency at which $|h_{fe}|$ extrapolates to unity.



- L1 1-3/4 Turns, #18 AWG, 0.5" L, 0.5" Diameter
- L2 2 Turns, #16 AWG, 0.5" L, 0.5" Diameter
- L3 2 Turns, #13 AWG, 0.25" L, 0.5" Diameter (Position 1/4" from L2)

Figure 1. 200 MHz Amplifier Power Gain and Noise Figure Circuit

PACKAGE DIMENSIONS



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. CONTROLLING PERCHAGE BEYOND DIMENSION R IS UNCONTROLLED.
 4. DIMENSION F APPLIES BETWEEN P AND L. DIMENSION DAND J APPLY BETWEEN L AND K MINIMUM. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
С	0.125	0.165	3.18	4.19
D	0.016	0.022	0.41	0.55
F	0.016	0.019	0.41	0.48
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500		12.70	
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
Р		0.100		2.54
R	0.115		2.93	
٧	0.135	_	3.43	

STYLE 1: PIN 1. EMITTER

2. BASE 3. COLLECTOR

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