

SSRP130B1

 $\begin{array}{c} \mbox{Application Specific Discretes} \\ \mbox{A.S.D.}^{\rm TM} \end{array}$

DUAL ASYMMETRICAL OVERVOLTAGE PROTECTION FOR TELECOM LINE

MAIN APPICATIONS

Where asymmetrical protection against lightning strikes and other transient overvoltages is required :

The SSRP130B1 is a dual asymmetrical transient

voltage suppressor designed to protect a solid-state ring relay or SLICs with integrated ring

The asymmetrical protection configuration is necessary to allow the use of all different types of

Dual bidirectional asymmetrical protection :

■ Peak pulse current : !Pr = 2 * 25A (5 / 310; is)

Solid-State relays

DESCRIPTION

ringing schemes.

FEATURES

SLIC with integrated ring generator

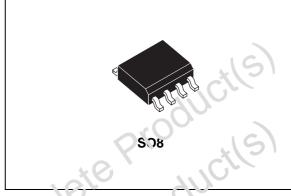
generator from overvoltages.

Stand-off voltages :

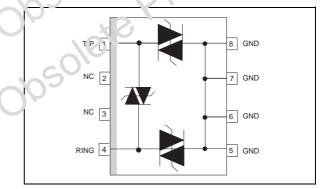
■ Holding current : 150n A

+ 130V for positive voltages

- 185V for negative voltages



FUNCTIONAL DIAGRAM



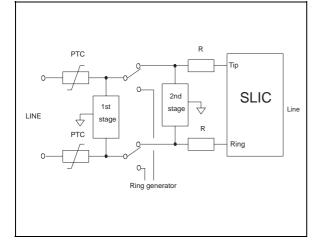
COMPLIES WITH THE FOLLOWING STANDARDS:	Peak Surge Voltage (V)	Voltage Waveform (μs)	Current Waveform (μs)	Admissible Ipp (A)	Necessary Resistor (Ω)
ITU-T K20	1000	10/700	5/310	25	-
VDE(1432	2000	10/700	5/310	25	40
VUFJ878	1500	1.2/50	1/20	35	3
NEC 1000-4-5	Level 2	10/700 1.2/50	5/310 8/20	25 25	-
FCC Part 68	1500 800	10/160 10/560	10/160 10/560	29 21	45 30
BELLCORE TR-NWT-001089	2500 1000	2/10 10/1000	2/10 10/1000	70 15	30 57

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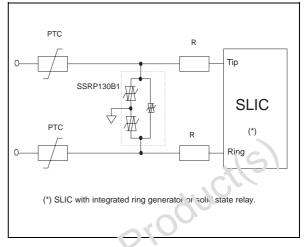
APPLICATION INFORMATION

Fig 1: Topology of the classical line card protection.



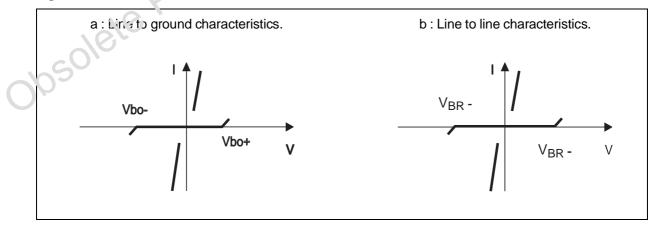
The classical line card requires protection before the ring relay and a second one for the SLIC (fig.1). The use of new SLICs with integrated ring generator or board based on solid state ring relay suppresses this second protection (Fig. 2). Then the only remaining stage, located between the line and the ring relay, has to optimize the protection. The classical symmetrical first stage protector becomes not sufficient to avoid any circuit destruction during surges.

Fig 2: Classical use of the SSRP130B1.



The SSRP13059 device takes into account this fact and is based on asymmetrical voltage characteristics (Fig.3a). The ring signal being shifted back by the battery voltage, the SSRP130B1 negative breakover value Vbo- is greater than the positive one Vbo+. This point guarantees a protection operation very close to the peak of the normal operating voltage without any disturbance of the ring signal.

Fig 3: SSRP130B e ectrical characteristics.



In addition with the 2 crowbar functions which perform the protection of both TIP and RING lines versus ground, a third cell assumes the differential mode protection of the SLIC. The breakdown voltage values of this third cell are the same for both positive and negative parts of the characteristics and are equivalent to the negative breakdown voltage value of the TIP and RING lines versus GND cells (Fig.3 b).

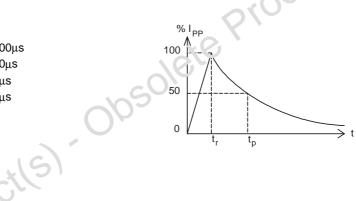
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ABSOLUTE MAXIMUM RATINGS ($T_{amb} = 25^{\circ}C$)

Symbol	Parameter	Value	Unit	
Ірр	Peak pulse current (see note 1)	10/1000 μs 5/310μs 1/20μs 2/10μs	2x15 2x25 2x35 2x70	A
Ітѕм	Non repetitive surge peak on-state current (F=50Hz)	tp = 0.2 s tp = 5 s tp = 900 s	7.5 4.0 1.5	A
T _{op}	Operating temperature range	·	0 to + 70	°C
T _{stg} Tj	Storage temperature range Maximum operating junction temperature	- 55 to + 150 + 150	ن م ک	
TL	Maximum lead temperature for soldering during 10	26.7	°C	

Note 1 : Pulse waveform :

10/1000µs	t _r =10μs	t _p =1000μs
5/310µs	t _r =5µs	t _p =310μs
1/20µs	t _r =1µs	t _p =20μs
2/10µs	t _r =2µs	t _p =10μs

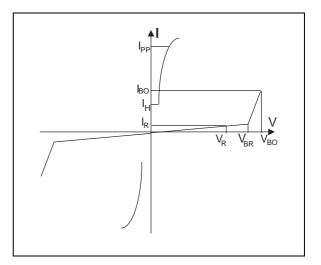


THERMAL RESISTANCE

Symbol	Parameter	Value	Unit
R _{th (j-a)}	Junction to ambient	170	°C/W

ELECTRICAL CHARACTERISTICS (Tamb = 25°C)

Symbol	Parameter					
VR	Stand-off voltage					
I _R	Leakage current at stand-off voltage					
V _{BR}	Breakdown voltage					
V _{BO}	Breakover voltage					
Iн	Holding current					
Іво	Breakover current					
IPP	Peak pulse current					
С	Capacitance					



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ELECTRICAL CHARACTERISTICS between TIP and GND, RING and GND (Tamb=25°C)

Symbol	Parameter	Test conditions (note 1)	Min	Max	Unit
V _{BO}	Breakover voltage (note 2)	Positive voltage 50Hz 10/700μs 1.2/50μs 2/10μs		200 175 180 250	V
		Negative voltage 50Hz 10/700μs 1.2/50μs 2/10μs		280 235 240 340	5
I _{BO}	Breakover current	Positive voltage Negative voltage	110 110	5	mA
Ін	Holding current	Positive polarity Negative polarity	150 150		mA
I _R	Leakage current (note 3)	V _R = +130 V V _R = - 185 V		10 10	μΑ
С	Capacitance	F = 100kHz, V = 100n, V, VR = 0V		100	pF

ELECTRICAL CHARACTERISTICS between TiP and RING (Tamb=25°C)

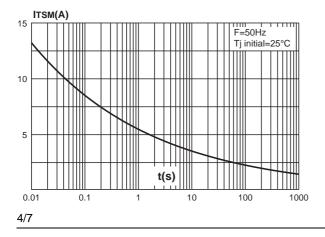
Symbol	Parameter	Test conditions	Min	Max	Unit
I _R	Leakage current (note 3)	V _R = +185 V V _R = - 185 V		10 10	μΑ
С	Capacitarice	$F = 100kHz$, $V = 100mV$, $V_R = 0V$		100	рF

Positive voltage neans between T and G, or between R and G Note 1 :

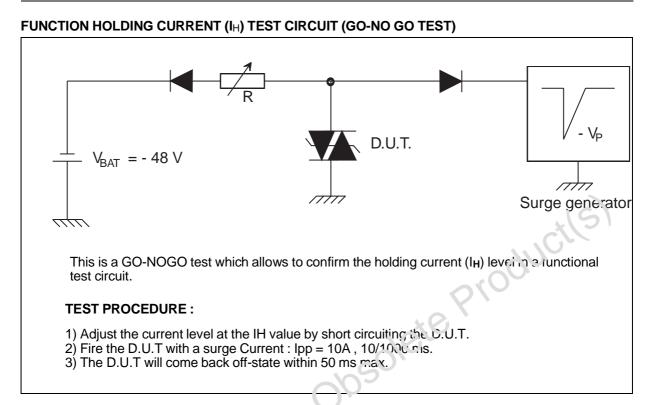
Negative voltage means between G and T, or between G and T See tection of the VBO parameters $P_R = 0.5$ and V_R guarantees $V_{BR} > V_R$

Note 2 : Note 3 :

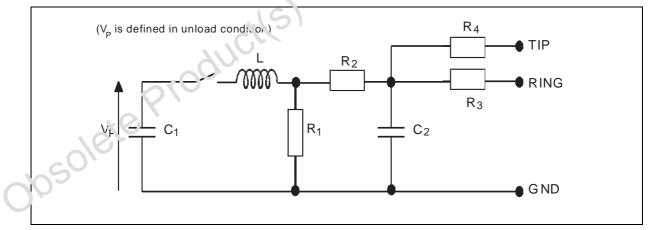
.Fic. 4 : Surge peak current versus overload duration (maximum values).



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TEST CIRCUIT FOR VBO parameters :

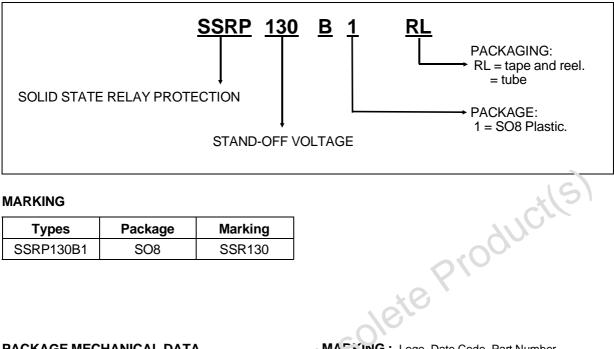


Pulse	e (μs)	Vp	C ₁	C ₂	L	R 1	R ₂	R ₃	R 4	IPP	Rp
tr	tp	(V)	(μF)	(nF)	(μ Η)	(Ω)	(Ω)	(Ω)	(Ω)	(A)	(Ω)
10	700	1000	20	200	0	50	15	25	25	25	0
1.2	50	1500	1	33	0	76	13	25	25	30	10
2	10	2500	10	0	1.1	1.3	0	3	3	38	62

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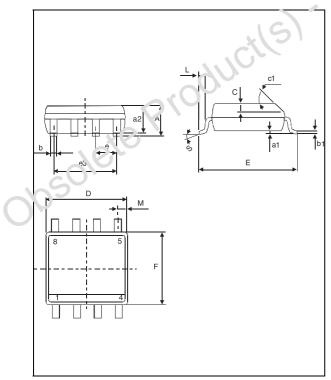


MARKING

Types	Package	Marking
SSRP130B1	SO8	SSR130

PACKAGE MECHANICAL DATA.

SO8 Plastic



Packaging : Products supplied in antistatic tubes or tape and reel.

Weight: 0.08g

MARTING: Logo, Date Code, Part Number.

	DIMENSIONS								
REF.	Mi	llimetr	es	Inches					
	Min.	Тур.	Max.	Min.	Тур.	Max.			
А			1.75			0.069			
a1	0.1		0.25	0.004		0.010			
a2			1.65			0.065			
b	0.35		0.48	0.014		0.019			
b1	0.19		0.25	0.007		0.010			
С		0.50			0.020				
c1		-	45°	(typ)					
D	4.8		5.0	0.189		0.197			
Е	5.8		6.2	0.228		0.244			
е		1.27			0.050				
e3		3.81			0.150				
F	3.8		4.0	0.15		0.157			
L	0.4		1.27	0.016		0.050			
М			0.6			0.024			
S	8° (max)								

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