

NUF2030XV6, NUF2042XV6

USB Upstream Terminator with ESD Protection

These devices are designed for applications requiring **Line Termination**, **EMI Filtering** and **ESD Protection**. They are intended for use in upstream USB ports, cellular phones, wireless equipment and computer applications. These devices offer an integrated solution in a small package (SOT-563) reducing PCB space and cost.

Features:

- Provides USB Line Termination, Filtering and ESD Protection
- Single IC Offers Cost Savings
- Bidirectional EMI Filtering Prevents Noise from Entering/Leaving the System
- Compliance with IEC61000-4-2 (Level 4)
 - 8 kV (Contact)
 - 15 kV (Air)
- ESD Ratings: Machine Model = C
Human Body Model = 3B
- These are Pb-Free Devices

Benefits:

- SOT-563 Package Minimizes PCB Space
- Integrated Circuit Increases System Reliability versus Discrete Component Implementation
- TVs Devices Provide ESD Protection That is Better than a Discrete Implementation because the Small IC minimizes Parasitic Inductances

Typical Applications:

- USB Hubs
- Computer Peripherals Using USB

MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$)

| Rating | Symbol | Value | Unit |
|---|------------------|-------------|------------------|
| Steady State Power | P_D | 225 | mW |
| Maximum Junction Temperature | $T_{J(\max)}$ | 125 | $^\circ\text{C}$ |
| Operating Temperature Range | T_J | -55 to +125 | $^\circ\text{C}$ |
| Storage Temperature Range | T_{stg} | -55 to +125 | $^\circ\text{C}$ |
| Lead Solder Temperature (10 second duration) | T_L | 260 | $^\circ\text{C}$ |

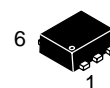
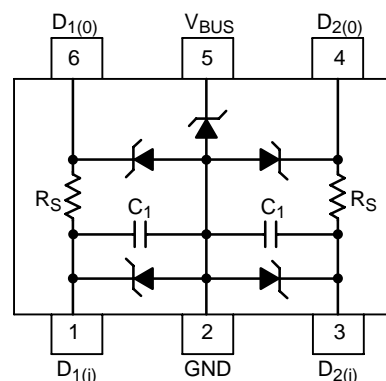
Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.



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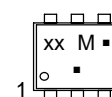
<http://onsemi.com>

CIRCUIT DESCRIPTION



SOT-563
CASE 463A

MARKING DIAGRAM



xx = Specific Device Code
(see table on page 5)
M = Month Code
▪ = Pb-Free Package
(Note: Microdot may be in either location)

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

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ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

| Device | V _{RWM} (V) | V _{BR} @ 1 mA (V) | | | I _R @ 3.3 V (nA) | | | Line Capacitance V _{dc} = 2.5 V f = 1 MHz (pF) (Note 1) | | | Series Resistor R _S (Ω) | | |
|--------------|-------------------------|-------------------------------|-----|-----|--------------------------------|-----|-----|---|-----|------|---------------------------------------|-----|------|
| | | Min | Typ | Max | Min | Typ | Max | Min | Typ | Max | Min | Typ | Max |
| NUF2030XV6T1 | 5.25 | 5.6 | 6.8 | 8.0 | – | 10 | 100 | – | 30 | 36 | 17.6 | 22 | 26.4 |
| NUF2042XV6T1 | 5.25 | 5.6 | 6.8 | 8.0 | – | 10 | 100 | 37.6 | 42 | 56.4 | 17.6 | 22 | 26.4 |

1. Measured between pins 1, 3, 4, 6 and ground with pin 5 also grounded.

2. For other resistance value (e.g. 33 Ω), please contact your local ON Semiconductor sales representative.

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TYPICAL CHARACTERISTICS

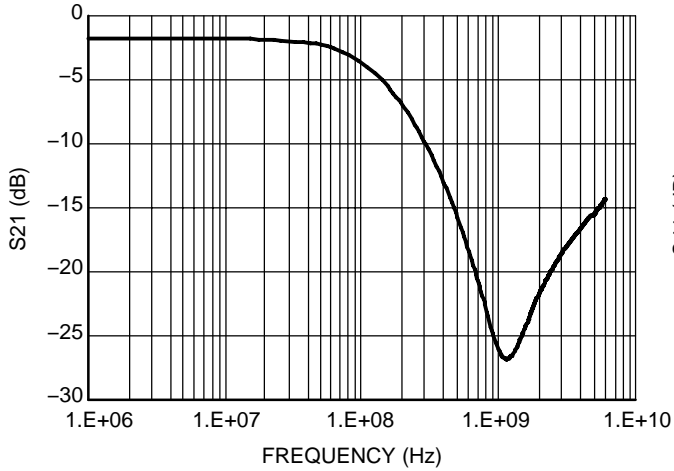


Figure 1. Insertion Loss Characteristics (NUF2030)

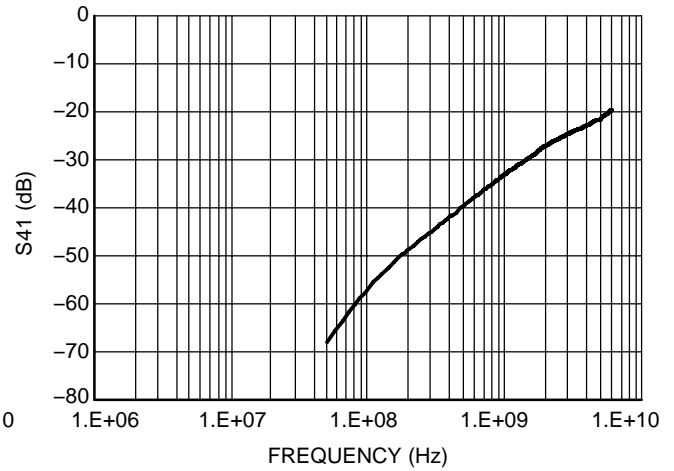


Figure 2. Analog Cross-Talk (NUF2030)

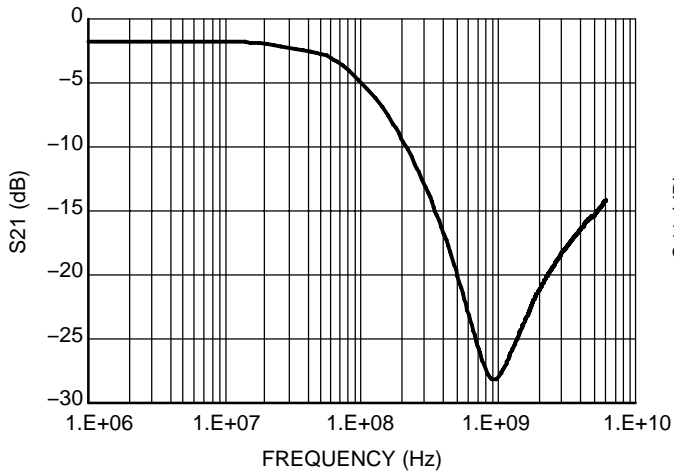


Figure 3. Insertion Loss Characteristics (NUF2042)

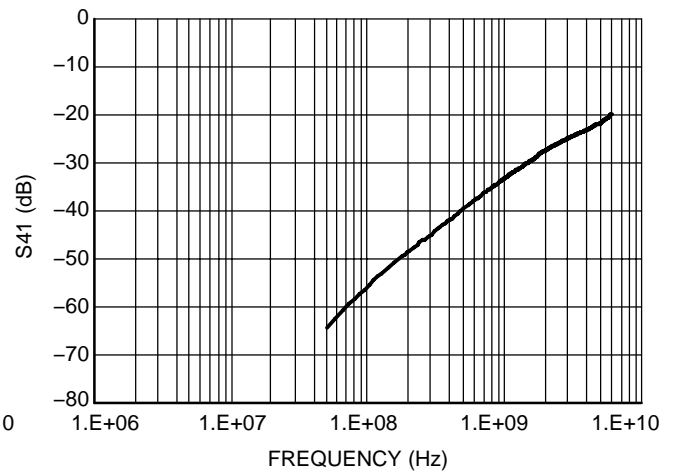


Figure 4. Analog Cross-Talk (NUF2042)

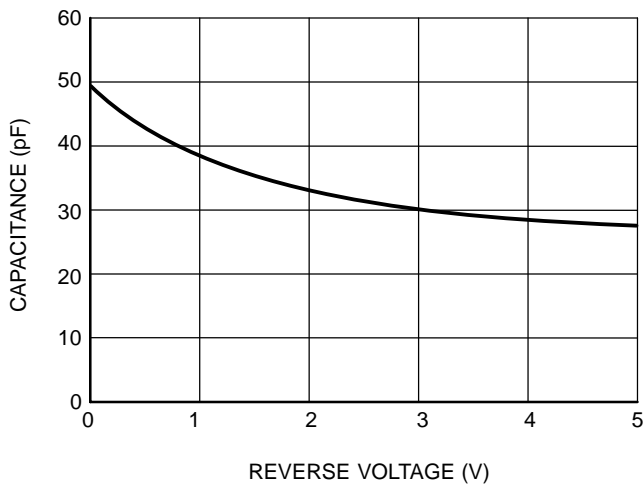


Figure 5. Typical Capacitance (NUF2030)

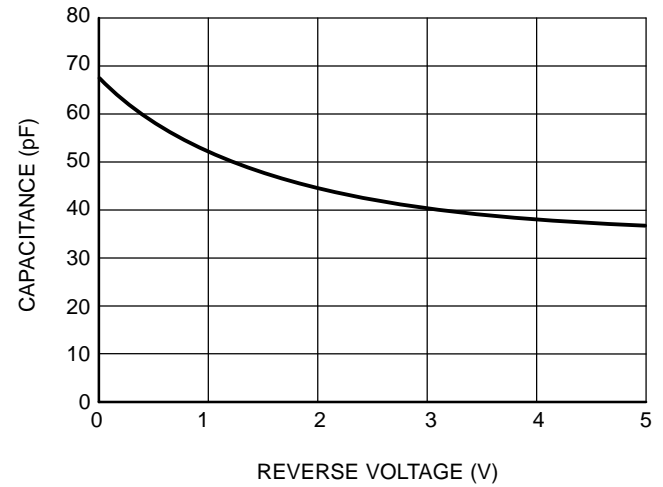
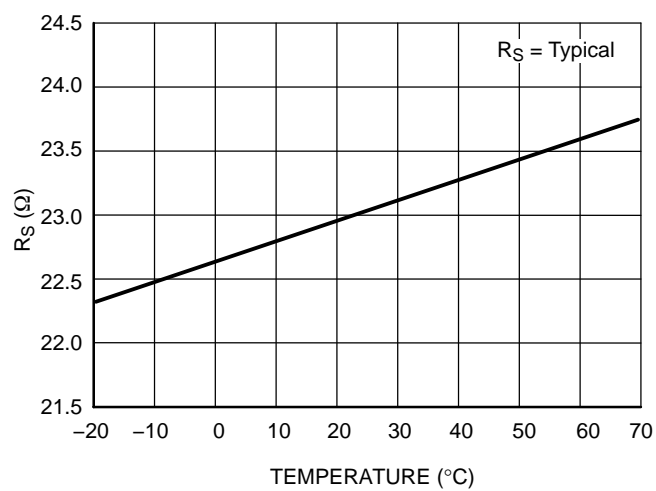


Figure 6. Typical Capacitance (NUF2042)

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**Figure 7. R_S versus Temperature
(NUF2030 and NUF2042)**

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ORDERING INFORMATION

| Device | Device Marking | Package | Shipping [†] |
|---------------|----------------|----------|-----------------------|
| NUF2030XV6T1 | 30 | SOT-563* | 4000 / Tape & Reel |
| NUF2030XV6T1G | 30 | SOT-563* | 4000 / Tape & Reel |
| NUF2042XV6T1 | 22 | SOT-563* | 4000 / Tape & Reel |
| NUF2042XV6T1G | 22 | SOT-563* | 4000 / Tape & Reel |

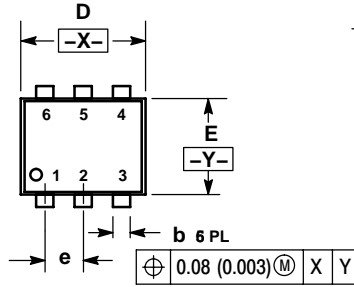
[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

*These packages are inherently Pb-Free.

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PACKAGE DIMENSIONS

SOT-563, 6 LEAD
CASE 463A-01
ISSUE E

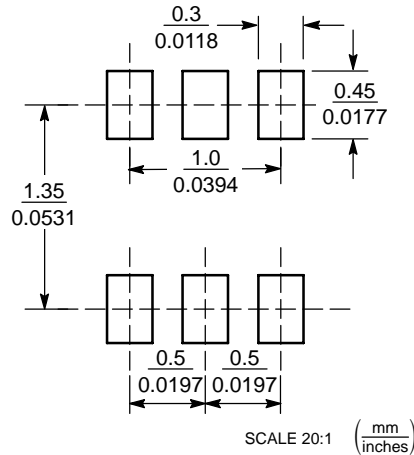


NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETERS
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.

| DIM | MILLIMETERS | | | INCHES | | |
|----------------|-------------|------|------|----------|-------|-------|
| | MIN | NOM | MAX | MIN | NOM | MAX |
| A | 0.50 | 0.55 | 0.60 | 0.020 | 0.021 | 0.023 |
| b | 0.17 | 0.22 | 0.27 | 0.007 | 0.009 | 0.011 |
| C | 0.08 | 0.12 | 0.18 | 0.003 | 0.005 | 0.007 |
| D | 1.50 | 1.60 | 1.70 | 0.059 | 0.062 | 0.066 |
| E | 1.10 | 1.20 | 1.30 | 0.043 | 0.047 | 0.051 |
| e | 0.5 BSC | | | 0.02 BSC | | |
| L | 0.10 | 0.20 | 0.30 | 0.004 | 0.008 | 0.012 |
| H _E | 1.50 | 1.60 | 1.70 | 0.059 | 0.062 | 0.066 |

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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