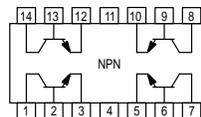


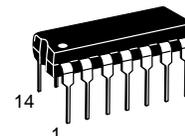
Quad Switching Transistor

NPN Silicon



MPQ2369

Motorola Preferred Device



CASE 646-06, STYLE 1
TO-116

MAXIMUM RATINGS

Rating	Symbol	Value		Unit
Collector–Emitter Voltage	V_{CEO}	15		Vdc
Collector–Base Voltage	V_{CBO}	40		Vdc
Emitter–Base Voltage	V_{EBO}	4.5		Vdc
Collector Current — Continuous	I_C	500		mAdc
		Each Transistor	Total Device	
Total Device Dissipation @ $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	0.5 5.0	1.5 15	Watts mW/ $^\circ\text{C}$
Operating and Storage Junction Temperature Range	T_J, T_{stg}	–55 to +125		$^\circ\text{C}$

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	83	$^\circ\text{C}/\text{W}$

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
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OFF CHARACTERISTICS

Collector–Emitter Breakdown Voltage ⁽¹⁾ ($I_C = 10 \text{ mAdc}$, $I_B = 0$)	$V_{(BR)CEO}$	15	—	—	Vdc
Collector–Base Breakdown Voltage ($I_C = 10 \mu\text{Adc}$, $I_E = 0$)	$V_{(BR)CBO}$	40	—	—	Vdc
Emitter–Base Breakdown Voltage ($I_E = 10 \mu\text{Adc}$, $I_C = 0$)	$V_{(BR)EBO}$	4.5	—	—	Vdc
Collector Cutoff Current ($V_{CB} = 20 \text{ Vdc}$, $I_E = 0$)	I_{CBO}	—	—	0.4	μAdc

1. Pulse Test: Pulse Width $\leq 300 \mu\text{s}$; Duty Cycle $\leq 2.0\%$.

Preferred devices are Motorola recommended choices for future use and best overall value.

MPQ2369**ELECTRICAL CHARACTERISTICS** ($T_A = 25^\circ\text{C}$ unless otherwise noted) (Continued)

Characteristic	Symbol	Min	Typ	Max	Unit
ON CHARACTERISTICS					
DC Current Gain ⁽¹⁾ ($I_C = 10\text{ mAdc}$, $V_{CE} = 1.0\text{ Vdc}$) ($I_C = 100\text{ mAdc}$, $V_{CE} = 2.0\text{ Vdc}$)	h_{FE}	40 20	— —	— —	—
Collector–Emitter Saturation Voltage ($I_C = 10\text{ mAdc}$, $I_B = 1.0\text{ mAdc}$)	$V_{CE(sat)}$	—	—	0.25	Vdc
Base–Emitter Saturation Voltage ($I_C = 10\text{ mAdc}$, $I_B = 1.0\text{ mAdc}$)	$V_{BE(sat)}$	—	—	0.9	Vdc

SMALL–SIGNAL CHARACTERISTICS

Current–Gain — Bandwidth Product ($I_C = 10\text{ mAdc}$, $V_{CE} = 10\text{ Vdc}$, $f = 100\text{ MHz}$)	f_T	450	550	—	MHz
Output Capacitance ($V_{CB} = 5.0\text{ Vdc}$, $I_E = 0$, $f = 1.0\text{ MHz}$)	C_{obo}	—	2.5	4.0	pF
Input Capacitance ($V_{EB} = 0.5\text{ Vdc}$, $I_C = 0$, $f = 1.0\text{ MHz}$)	C_{ibo}	—	3.0	5.0	pF

SWITCHING CHARACTERISTICS

Turn–On Time ($V_{CC} = 3.0\text{ Vdc}$, $V_{BE} = 1.5\text{ Vdc}$, $I_C = 10\text{ mAdc}$, $I_{B1} = 3.0\text{ mAdc}$)	t_{on}	—	9.0	—	ns
Turn–Off Time ($V_{CC} = 3.0\text{ Vdc}$, $I_C = 10\text{ mAdc}$, $I_{B1} = 3.0\text{ mAdc}$, $I_{B2} = 1.5\text{ mAdc}$)	t_{off}	—	15	—	ns

1. Pulse Test: Pulse Width $\leq 300\ \mu\text{s}$; Duty Cycle $\leq 2.0\%$.

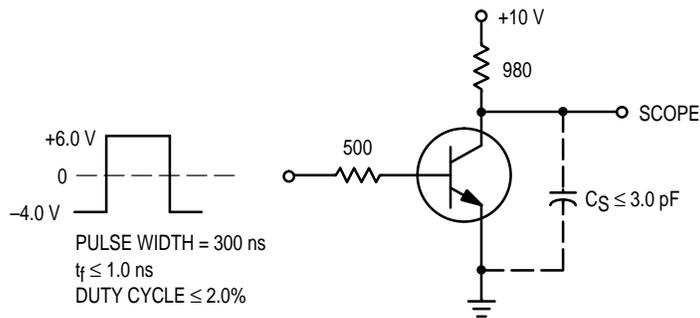


Figure 1. Storage Time Test Circuit

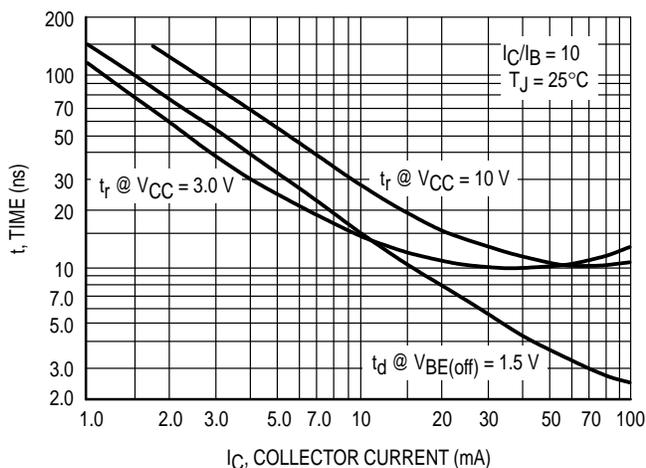


Figure 2. Turn-On Time

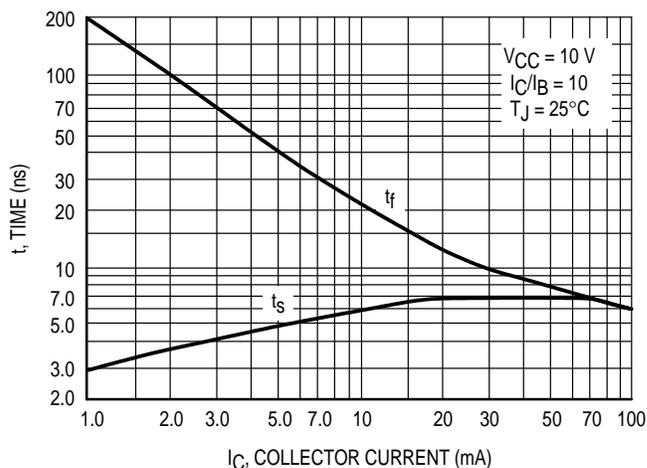


Figure 3. Turn-Off Time

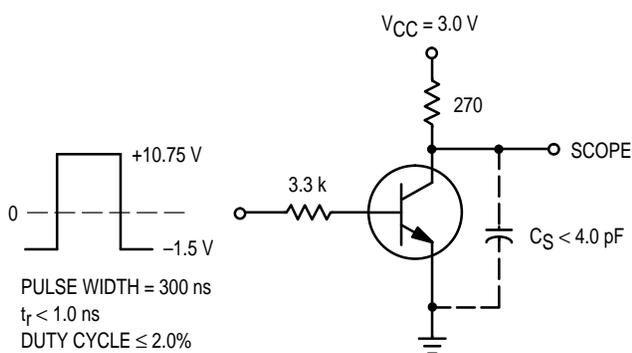


Figure 4. Turn-On Test Circuit

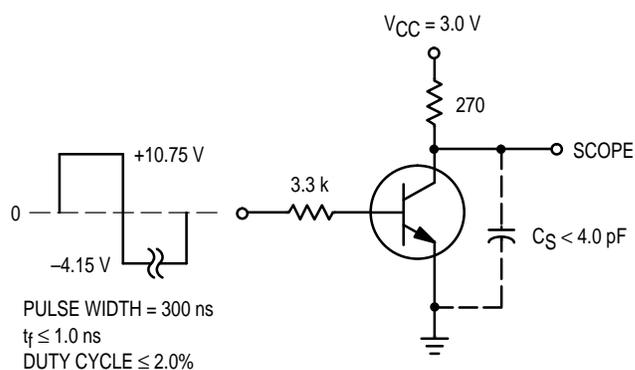


Figure 5. Turn-Off Test Circuit

MPQ2369

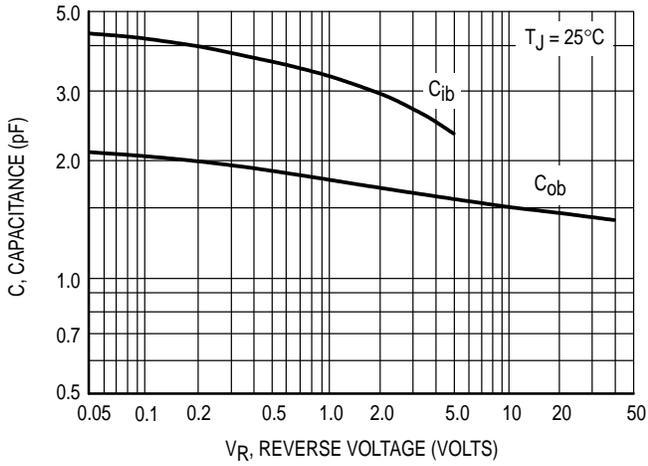


Figure 6. Capacitance

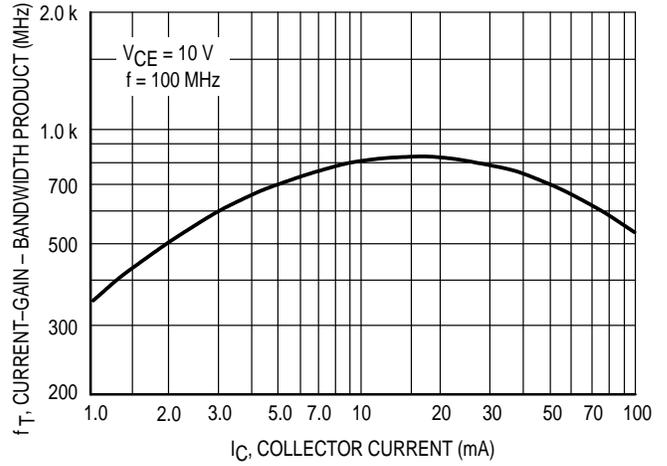


Figure 7. Current-Gain — Bandwidth Product

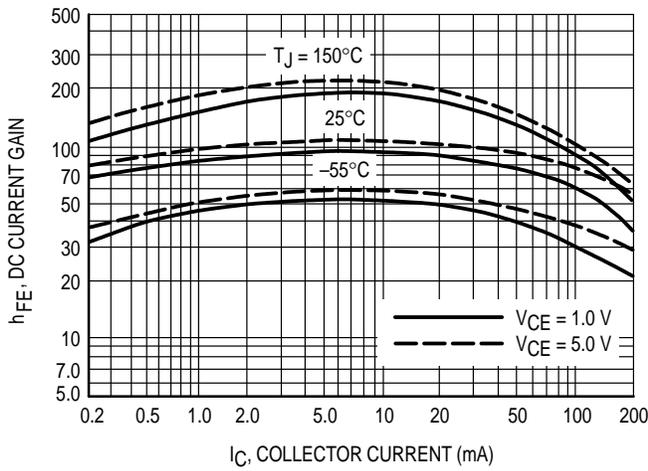


Figure 8. DC Current Gain

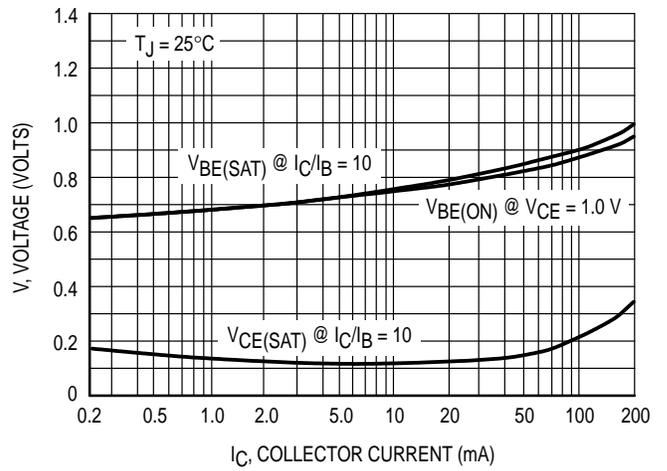


Figure 9. "ON" Voltages

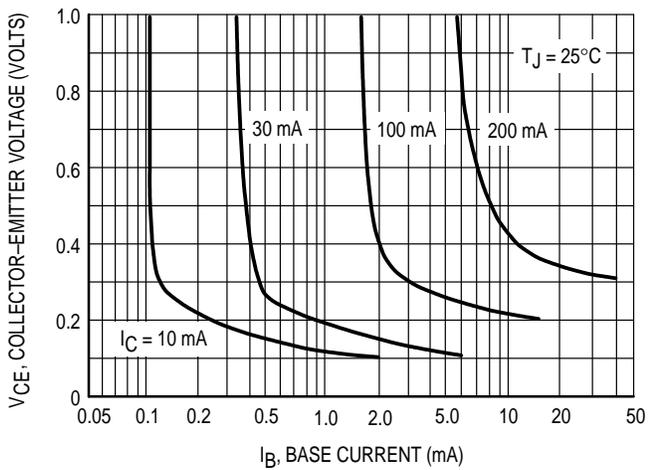


Figure 10. Collector Saturation Region

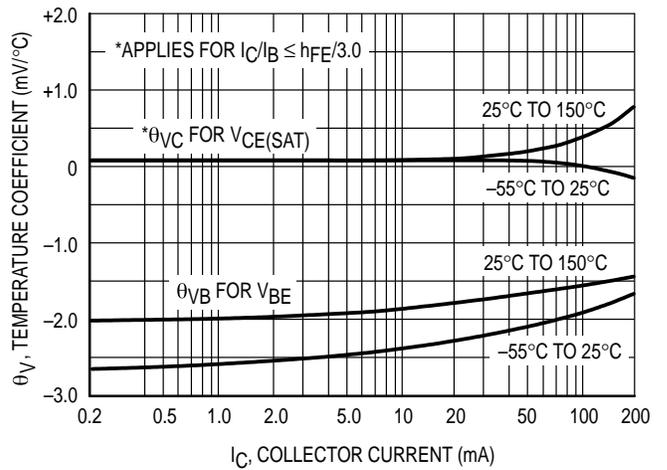
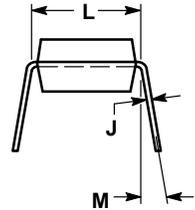
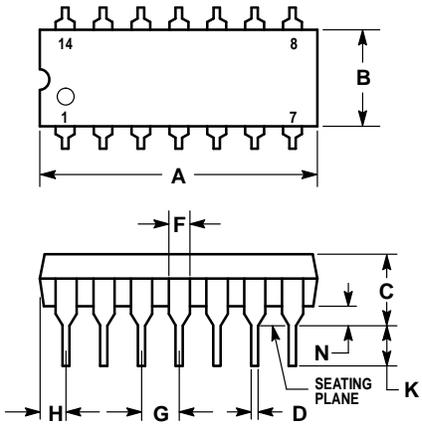


Figure 11. Temperature Coefficients

PACKAGE DIMENSIONS



- STYLE 1:
 PIN 1. COLLECTOR
 2. BASE
 3. EMITTER
 4. NO CONNECTION
 5. EMITTER
 6. BASE
 7. COLLECTOR
 8. COLLECTOR
 9. BASE
 10. EMITTER
 11. NO CONNECTION
 12. EMITTER
 13. BASE
 14. COLLECTOR

- NOTES:
 1. LEADS WITHIN 0.13 (0.005) RADIUS OF TRUE POSITION AT SEATING PLANE AT MAXIMUM MATERIAL CONDITION.
 2. DIMENSION L TO CENTER OF LEADS WHEN FORMED PARALLEL.
 3. DIMENSION B DOES NOT INCLUDE MOLD FLASH.
 4. ROUNDED CORNERS OPTIONAL.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.715	0.770	18.16	19.56
B	0.240	0.260	6.10	6.60
C	0.145	0.185	3.69	4.69
D	0.015	0.021	0.38	0.53
F	0.040	0.070	1.02	1.78
G	0.100 BSC		2.54 BSC	
H	0.052	0.095	1.32	2.41
J	0.008	0.015	0.20	0.38
K	0.115	0.135	2.92	3.43
L	0.300 BSC		7.62 BSC	
M	0° - 10°		0° - 10°	
N	0.015	0.039	0.39	1.01

CASE 646-06
 TO-116
 ISSUE M

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